

Study Committee D1

Materials and Emerging Test Techniques

Paper D1-PS2-10500

Recommendations for IEC 60815-2 based on Functional Performance of Optimized HVCB Porcelain Insulators in Very Highly Polluted Environments

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Motivation

- Limited research on HVCBs for High Pollution environments (PM2.5 and High SO₂ Concentrations)
- High Rate of Pollution Flashovers in 765kV HVCBs
- Opportunity to Re-Work and Enhance Profile parameters guidelines in IEC_60815-1
- Expensive and Time-Consuming Testing Methods and Arduous Setup Procedure for each iteration of the Artificial Pollution Test
- Optimization of Yield Output of Porcelain insulators and Material usage in production
- Reduced Carbon footprint by decreasing porcelain base materials

Method/Approach

- Review of existing studies on
	- Pollution Performance of Porcelain Insulators (IEC Standards)
	- Air Pollution in India / Pollution Conditions PM2.5 and PM10
	- Manufacturing Constraints Pug Baking, Glazing (Indian Insulator Mfg. companies)
	- Artificial Pollution Salt Fog Test at UHVRL
- Analytical methods were used to estimate
	- Peak Performance of 765kV Insulators and threshold limit (w.r.t. IEC standards) for High Creepage insulators.
	- Areas for further research in design optimization of EHV CBs
	- Safety tolerance to adjust profile parameters for increased material optimization and design value.

Objects of Investigation

- This paper aims to prove that "Hollow Type Porcelain Insulators" designed for the insulation block, with multiple profile parameters falling in the Major deviation zone of section 6.1 as per IEC 60815-2, will perform satisfactorily in severe pollution conditions
- The Salt fog test method is selected for the successful simulation of real-life pollution environments for this qualification at salinity 160 kg/m³ as per IEC 60507.

Experimental setup & test results

Level of Everyday Pollution at site: Very Heavy with PM2.5 range between 90-300 μ g/m³, PM10 ranging normally between 100-300 µg/m³

- The 765 kV transmission lines of India are predominantly situated along the very highly polluted coastal environments. Thus, HVCBs are subjected to high di-electric stress.
- Pollution flashover in outdoor porcelain insulators is influenced by profile parameters in insulator design.

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Insulator Design Parameters

- The Indian Standard recommends designing outdoor porcelain insulators for 31mm/kV (in millimetres per unit of Rated Voltage) for very heavily polluted atmospheres.
- The specifications for selection of unified specific creepage distance are listed in IEC:60815-1 as in the table below:
- Creepage Correlation b/w SCD and USCD:

The specific creepage distance (SCD) for Very Heavy level of pollution of 31mm/kV corresponds to USCD of 53,7mm/kV as recommended in Annex J of IEC 60815-1 for three-phase A.C. system. The phase-toearth voltage $U_{\rm v}/\sqrt{3}$ is considered to compute the USCD.

Shed profile design for polluted environments

- The pollution performance of insulators characterized by the shed profile parameters used in the design of the CB with deviation recommendations from IEC 60815-2.
- Guidelines on the design parameters stated, have a positive correlation on the pollution performance of the insulator, in the salt fog test as per IEC 60507.
- A partial arc formed by dry band activity in series with the contaminated region of the insulator is given by a series resistance.
- An increase in contamination causes the leakage current to increase, creating more intensive discharges. Increased thermal ionization caused by high temperature arcs, would promote flashover.
- Dry Band Phenomenon:

Significance of Deviations in design

Challenges in Insulator Design & Mfg

The design of such $AIS - EHV CB$ for very high pollution levels, remains complex with respect to Material and Strength. Raw materials are compressed to dense sheets removing excess moisture as shown.

Special tool on a VMC control creepage factor by maintaining the shed angle and overhang as per design in second phase as seen in Figure 5. In the third phase, Insulator are solidified in Isothermal kiln furnaces.

Lack of shed thickness optimisation, pug interference during feeding into kiln, cracking of overhangs after firing reduces yield of insulators, thereby decreasing quality repeatability of the process.

- In fourth Phase, Sheds are glazed as required which determines dust deposit and rainwater bridging.
- In the fifth Phase, flanges and Insulator are bonded together with cement to withstand service loads and seismic level of the Circuit breaker

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Pre-Conditioning Process

The test specimen is conditioned as per IEC 60507. Designed specimen withstood eight spells. The moisture and fog particles on the insulator shed prior to pre-conditioning.

Type Test as per IEC 60507

The USCD and I_{hmax} were calculated. The value of the ratio of minimum value of short circuit current to the maximum highest leakage current pulse amplitude met the standard IEC 60507.

$$
I_{sc\ min} = \text{USCD}/\sqrt{3} - 10 = 21 \text{ A}_{rms}
$$

$$
\frac{I_{sc}}{I_{h\ max}} \ge 11 \Rightarrow \frac{21}{1.7167} = 12.233 > 11
$$

Parametric Design Methodology and Mathematical Modelling of ST

• A mathematically modelled was developed comparing validated design with lenient design using parametric approach. The interpolation of % of change in profile parameters [l/S and l/d] for lenient and optimized design, for 31mm/kV SCD requirements has grown as a workable reference for designing high creepage insulators in future. The standard error S_T is the ratio of standard deviation of the sampling range to the square root of total number of samples i.e. 4.363%.

$$
erance (S_T) = \frac{2 \sum (x_i - \mu)^2}{N} = 4.362616\%
$$
\n
$$
energy distance (S_T) = \frac{N}{N} = 4.362616\%
$$

Safety Tol $N =$ Size of pans
 $x_i = V$ alue of pa
 $\mu =$ mean of pan

Safety Tolerance for New Designs

The fifth order polynomial equation derived for the optimized insulator can be compared with its reference curve as shown below.

 $y = -0.005x5 + 0.1044x4 - 0.8298x3 + 3.0541x2 - 5.0179x + 3.6994$

Functional Performance of Designed Insulator in Current Site Situatione

The designed circuit breaker is proven to successfully function and perform to above expectations in very highly polluted site conditions for more than 7 years without any complaints from the customer site.

Conclusion

• IEC recommends achieving a higher Creepage Factor to qualify the salt fog test. This research paper proposes to IEC to significantly relook the range of minor deviations recommended in Clause 9.5 and 9.7 of IEC 60815-2